

RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITING PROCEDURE
EXAMINING GROUP 2800

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To Examiner: Walter Lee Lindsay, Jr.	Total Pages Sent: 9
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

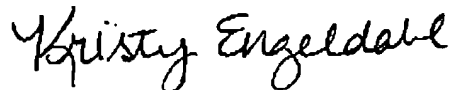
Applicant: Chen, *et al.* Docket No.: TSM03-0586
Serial No.: 10/723,771 Art Unit: 2812
Date Filed: November 26, 2003
TITLE: Method Of Fabricating High Voltage Transistor

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- Response Under 37 C.F.R. § 1.116 (8 pages)

Respectfully submitted,



Kristy Engeldahl
Legal Assistant

Confirmation Respectfully Requested

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Applicant respectfully submits the following amendments and remarks in response to the Final Office Action dated November 2, 2005. Applicant respectfully requests that these amendments and remarks be entered in pursuant to the provisions of 37 CFR § 1.116, and respectfully request reconsideration of Claims 1-18.